

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	21	703/27.ccls. and @pd>"20061001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/04/20 17:50

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	496	annotat\$4 and (performance or timing) and (simulat\$4 or emulat\$4) and @pd>"20061001"	US-PGPUB; USPAT; EPO; DERWENT	OR	ON	2007/04/20 17:59


[Web](#) [Images](#) [Video](#) [News](#) [Maps](#) [more »](#)





 All  
Sc  
Sc

**Scholar** [All articles](#) [Recent articles](#) Results **1 - 10** of about **2,060** for **annotation assembler level source c**
**All Results**
[D Burger](#)
[T Austin](#)
[S Bennett](#)
[G Necula](#)
[C Krueger](#)
[The SimpleScalar tool set, version 2.0 - group of 104 »](#)

 D Burger, TM Austin - ACM SIGARCH Computer Architecture News, 1997 - portal.acm.org  
 ... with **annotations** to instructions in the **assembly** files ... tions without having to change  
 and re.compile the **assembler**. ... bit 6 within the 16-bit **annotation** field) to ...

 Cited by 1015 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)
[... program which includes correlation of computer program source code  
 with optimized object code - group of 4 »](#)

GS Brooks, SM Simmons - US Patent 5,371,747, 1994 - Google Patents

 ... 262- 264- {LOOP LEVEL SOURCE UNITS} ... 6, 1994 Sheet 6 of 14 5,371,747 **SOURCE**  
**UNIT**
**ANNOTATIONS** 00000000001 1 1 1 1 1 1 1 1 01 2345 678 901 2345 678 R ...

 Cited by 31 - [Related Articles](#) - [Web Search](#)
[Software reuse - group of 17 »](#)

CW Krueger - ACM Computing Surveys (CSUR), 1992 - portal.acm.org

 ... of artifacts that can be reused are not limited to **source code** fragments but rather  
 may include design struc- tures, module-level implementation structures ...

 Cited by 573 - [Related Articles](#) - [Web Search](#)
[Compilation-based software performance estimation for system level design -  
 group of 5 »](#)

 M Lazarescu, JR Bammi, E Harcourt, L Lavagno, M ... - ... of the IEEE International High-  
**Level** Validation and Test ..., 2000 - doi.ieeeecs.org

 ... is performed on the CFG and the **assembly code**. ... was based on **source code** analysis  
 with timing **annotations**. ... paper we dis- cuss an **assembler-level** timing analysis ...

 Cited by 20 - [Related Articles](#) - [Web Search](#)
[Experiments with a program timing tool based on source-level timing  
 schema - group of 10 »](#)

CY Park, AC Shaw - Computer, 1991 - doi.ieeecomputersociety.org

 ... **level** language program directly through **annotations** that are ... ware simulation and  
 extensive **assembly** language streams. ... ic methods at the **source-language level** ...

 Cited by 225 - [Related Articles](#) - [Web Search](#)
[\[BOOK\] Evaluating Future Microprocessors: The SimpleScalar Tool Set - group  
 of 15 »](#)

DC Burger, TM Austin, S Bennett - 1996 - citeseer.csail.mit.edu

 ... pile, with **annotations** to instructions in the **assembly** files ... without having to change  
 and recompile the **assembler**. ... bit 6 within the 16-bit **annotation** field) to ...

 Cited by 1210 - [Related Articles](#) - [View as HTML](#) - [Web Search](#) - [Library Search](#) - [BL Direct](#)
[Software performance estimation strategies in a system-level design tool -  
 group of 15 »](#)

 JR Bammi, E Harcourt, W Kruitzer, L Lavagno, MT ... - Hardware/Software Codesign, 2000.  
 CODES 2000. Proceedings of ..., 2000 - ieeeexplore.ieee.org

 ... Similar **annotations** (not shown in the figure) also ... Virtual Machine simulation model,



Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "(assembler-level) &lt;and&gt; (pyr &gt;= 1913 &lt;and&gt; pyr &lt;= 2000)"

Your search matched 31 of 1551427 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

☒ e-mail

## » Search Options

[View Session History](#)
[New Search](#)

## Modify Search

(assembler-level) &lt;and&gt; (pyr &gt;= 1913 &lt;and&gt; pyr &lt;= 2000)

☐ Check to search only within this results set.

Display Format:



Citation



Citation &amp; Abstract

## » Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard




- ☐ 1. Vector pipelining, chaining, and speed on the IBM 3090 and Cray X-MP  
 Cheng, H.;  
Computer  
 Volume 22, Issue 9, Sept. 1989 Page(s):31 - 42, 44, 46  
 Digital Object Identifier 10.1109/2.35212  
[AbstractPlus](#) | Full Text: [PDF\(1072 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ 2. Experiences with automatic generation of audio band digital signal processing circuits  
 Rabaey, J.; Brodersen, R.;  
Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '86  
 Volume 11, Apr 1986 Page(s):1541 - 1544  
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 3. A compilation-based software estimation scheme for hardware/software co-simulation  
 Lajolo, M.; Lazarescu, M.; Sangiovanni-Vincentelli, A.;  
Hardware/Software Codesign, 1999. (CODES '99) Proceedings of the Seventh International Works  
 3-5 May 1999 Page(s):85 - 89  
 Digital Object Identifier 10.1109/HSC.1999.777398  
[AbstractPlus](#) | Full Text: [PDF\(348 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 4. Compilation-based software performance estimation for system-level design  
 Lazarescu, M.T.; Bammi, J.R.; Harcourt, E.; Lavagno, L.; Lajolo, M.;  
High-Level Design Validation and Test Workshop, 2000. Proceedings. IEEE International  
 8-10 Nov. 2000 Page(s):167 - 172  
 Digital Object Identifier 10.1109/HLDVT.2000.889579  
[AbstractPlus](#) | Full Text: [PDF\(484 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 5. Data flow transformations to detect results which are corrupted by hardware faults  
 Engel, H.;  
High-Assurance Systems Engineering Workshop, 1996. Proceedings. IEEE  
 21-22 Oct. 1996 Page(s):279 - 285  
 Digital Object Identifier 10.1109/HASE.1996.618609  
[AbstractPlus](#) | Full Text: [PDF\(520 KB\)](#) IEEE CNF



Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

Results for "( assembler-level&lt;in&gt;metadata )"

Your search matched 8 of 1551427 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

☒ e-mail

## » Search Options

[View Session History](#)
[New Search](#)

## Modify Search

( assembler-level&lt;in&gt;metadata )

☐ Check to search only within this results set

Display Format:

☒ Citation

☐ Citation & Abstract

## » Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard




- ☐ 1. Vector pipelining, chaining, and speed on the IBM 3090 and Cray X-MP  
Cheng, H.;  
Computer  
Volume 22, Issue 9, Sept. 1989 Page(s):31 - 42, 44, 46  
Digital Object Identifier 10.1109/2.35212  
[AbstractPlus](#) | Full Text: [PDF\(1072 KB\)](#) IEEE JNL  
[Rights and Permissions](#)
- ☐ 2. A partial-correctness semantics for modelling assembler programs  
Watson, G.; Fidge, C.;  
[Software Engineering and Formal Methods, 2003. Proceedings. First International Conference on](#)  
25-26 Sept. 2003 Page(s):82 - 90  
Digital Object Identifier 10.1109/SEFM.2003.1236210  
[AbstractPlus](#) | Full Text: [PDF\(282 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 3. Data flow transformations to detect results which are corrupted by hardware faults  
Engel, H.;  
[High-Assurance Systems Engineering Workshop, 1996. Proceedings., IEEE](#)  
21-22 Oct. 1996 Page(s):279 - 285  
Digital Object Identifier 10.1109/HASE.1996.618609  
[AbstractPlus](#) | Full Text: [PDF\(520 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 4. Experiences with automatic generation of audio band digital signal processing circuits  
Rabaey, J.; Brodersen, R.;  
[Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '86,](#)  
Volume 11, Apr 1986 Page(s):1541 - 1544  
[AbstractPlus](#) | Full Text: [PDF\(264 KB\)](#) IEEE CNF  
[Rights and Permissions](#)
- ☐ 5. Mastering startup costs in assembler-based compiled instruction-set simulation  
Amicel, R.; Bodin, F.;  
[Interaction between Compilers and Computer Architectures, 2002. Proceedings. Sixth Annual Worl](#)  
3 Feb. 2002 Page(s):39 - 44  
Digital Object Identifier 10.1109/INTERA.2002.995841  
[AbstractPlus](#) | Full Text: [PDF\(215 KB\)](#) IEEE CNF